

Fig. 1

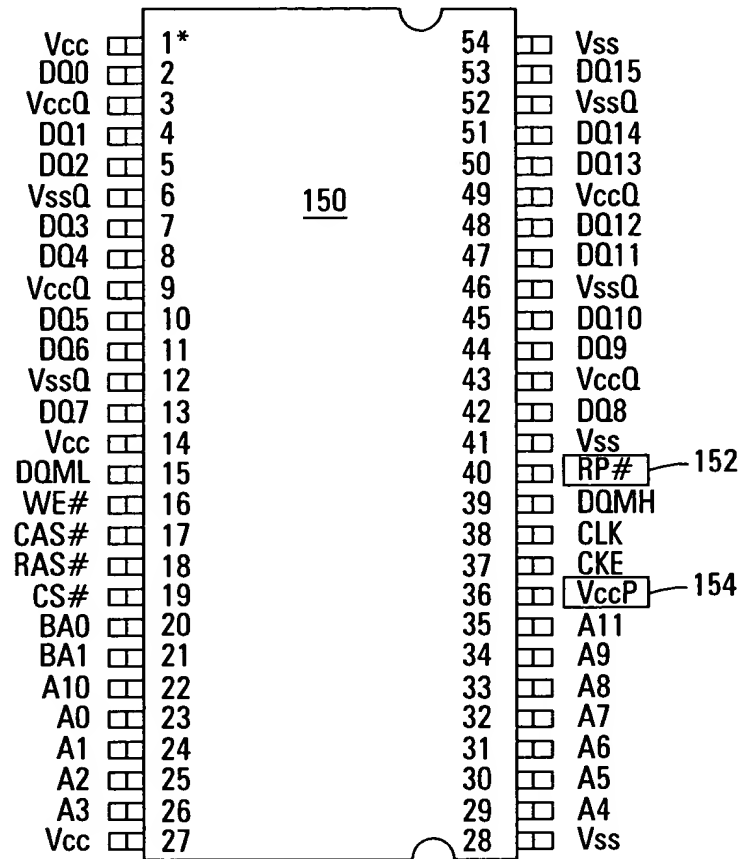


Fig. 2

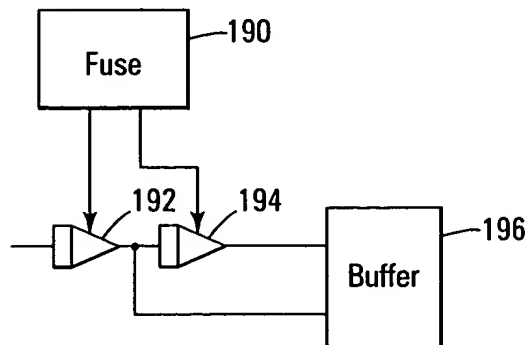


Fig. 3



First Named Inventor: Dean Nobunaga
Title: ADJUSTABLE TIMING CIRCUIT OF AN INTEGRATED CIRCUIT
Atty Docket No: 400.002US01
Filing Date: August 25, 2000 Serial No: 09/648,857

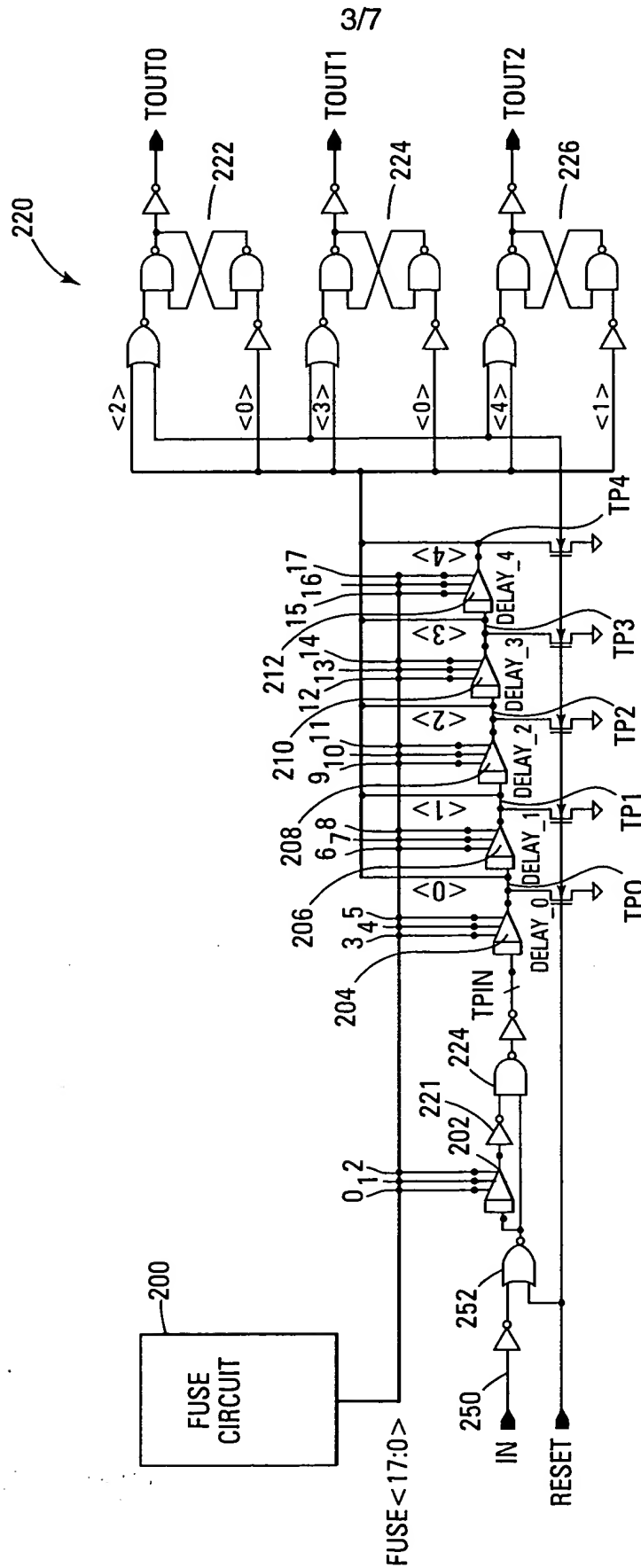


Fig. 4



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Atty Docket No: 400.002US01
Filing Date: August 25, 2000 Serial No: 09/648,857

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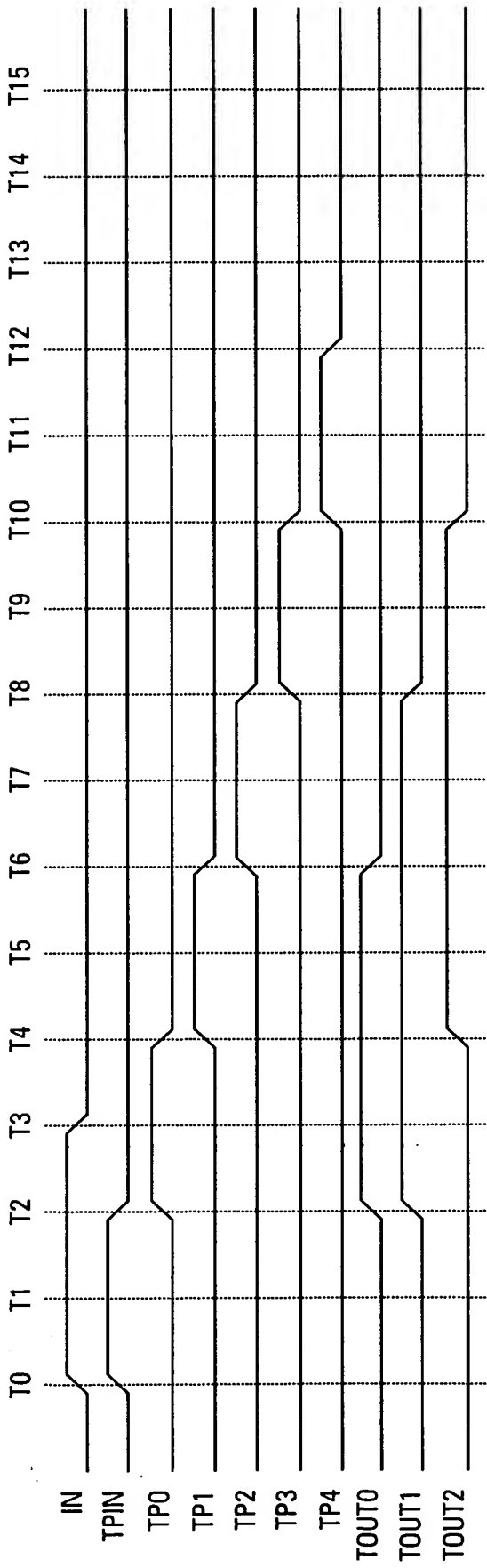


Fig. 5



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Atty Docket No: 400.002US01
Filing Date: August 25, 2000 Serial No: 09/648,857

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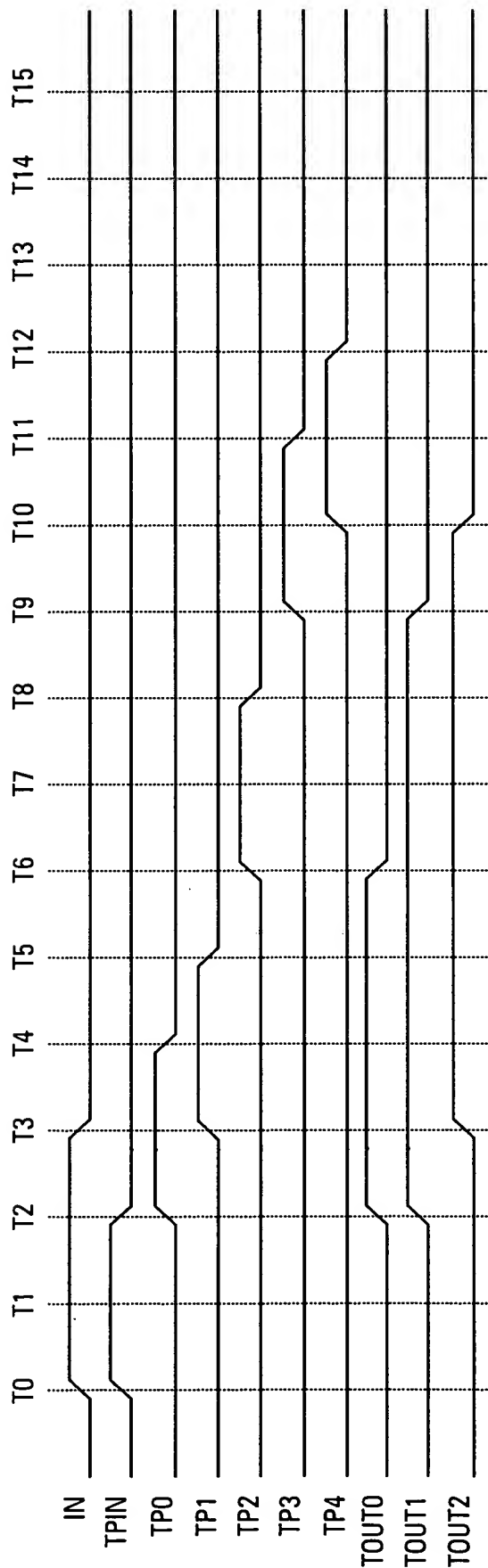


Fig. 6



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Atty Docket No: 400.002US01
Filing Date: August 25, 2000 Serial No: 09/648,857

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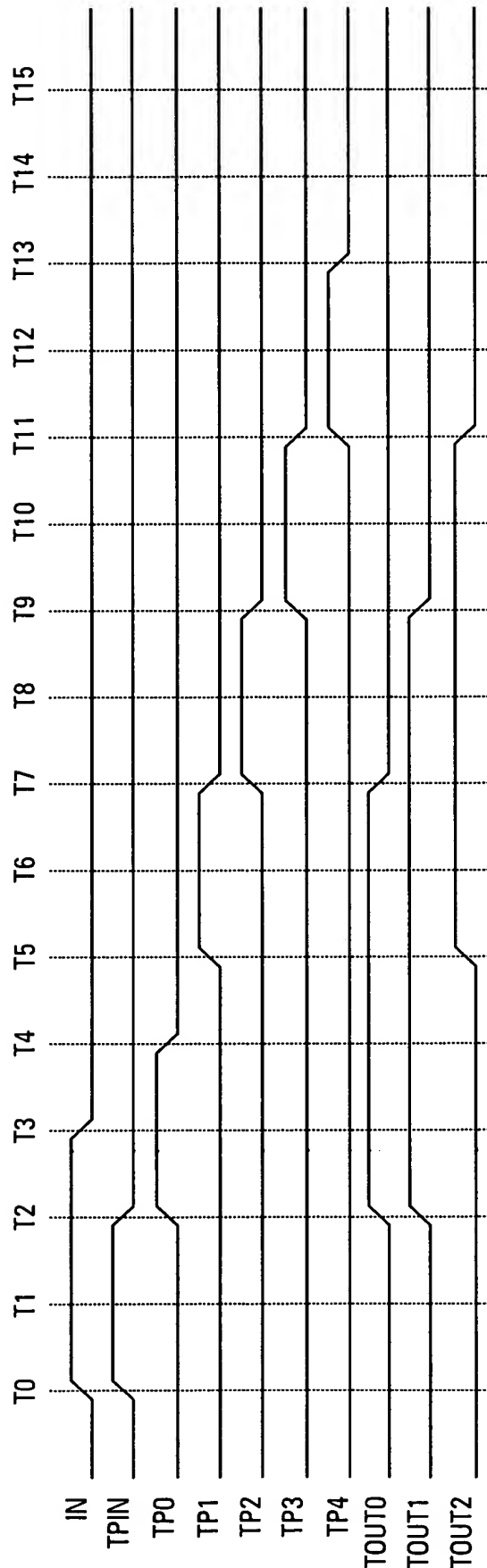


Fig. 7

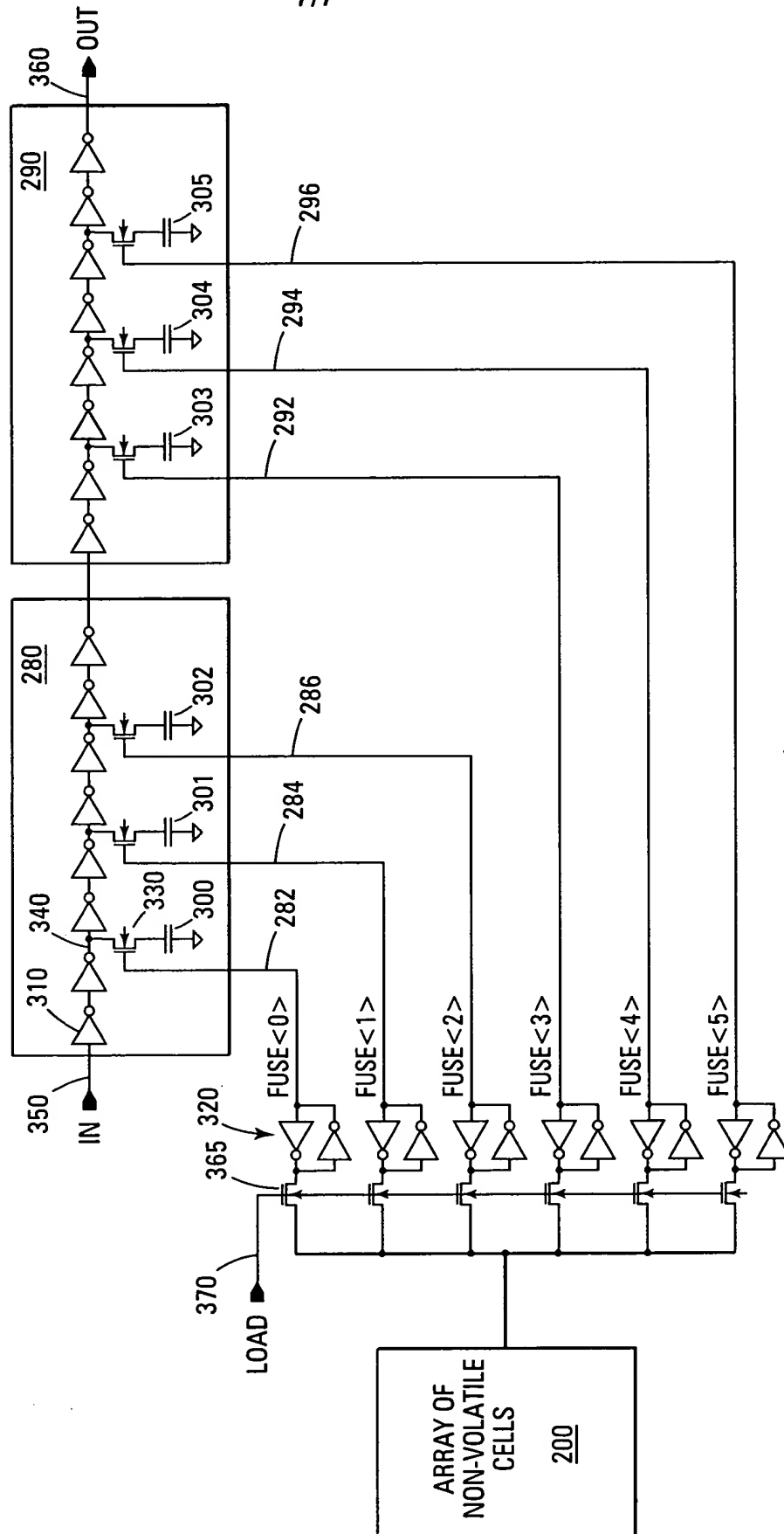


Fig. 8